

### **IN THE SPECIFICATION**

Please replace the two paragraphs starting on page 9 line 20 with the following two paragraphs:

The circuit 142 may be referred to as a binarization circuit. The binarization circuit 142 may be configured to perform a binarization operation on the signal EIN to generate an intermediate signal (e.g., INT1). The circuit 144 may be referred to as a context adaptive binary arithmetic coding (CABAC) circuit. The CABAC encoding circuit 144 may be configured to perform a context adaptive binary arithmetic coding of the signal ~~INT~~ INT1 based on the signal ctxIdxInc.

The CABAC encoding circuit 144 generally comprises a circuit (or module) 146 and a circuit (or module) 148. The circuit 146 may be referred to as a probability estimation circuit. The probability estimation circuit 146 may be configured to present a particular context among multiple context available to the circuit 148 based upon the signal ctxIdxInc. The circuit 148 may be referred to as a coding engine. The coding engine 148 may encode the signal INT1 using the particular context to generate the compressed data 108. The coding engine 148 may also generate a signal (e.g., ~~PDU~~ PEU) back to the probability estimation circuit 146. The signal ~~PDU~~ PEU may carry probability estimation update information to allow for adaptation over time to changing context.